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OCT 14 2004
PATENT & TRADEMARK OFFICE
S/N 09/706576

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Brian L. Schmidt et al.	Examiner:	Andrew M. Dolinar
Serial No.:	09/706576	Group Art Unit:	3747
Filed:	November 03, 2000	Docket:	279.268US1
Title:	CONFIGURATIONS AND METHODS FOR MAKING CAPACITOR CONNECTIONS		

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
MS: RCE
P.O. Box 1450
Alexandria, VA 22313-1450

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In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.



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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

BRIAN L. SCHMIDT ET AL.

By their Representatives,

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Date 10/12/04

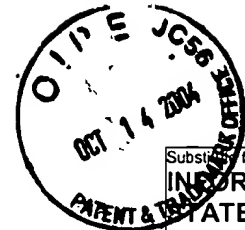
By Peter C. Maki
Peter C. Maki
Reg. No. 42,832

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, MS: RCE, P.O. Box 1450, Alexandria, VA 22313-1450, on this 12 day of October, 2004.

Paula Suchy
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Application Number	09/706576
Filing Date	November 3, 2000
First Named Inventor	Schmidt, Brian
Group Art Unit	3747
Examiner Name	Dolinar, Andrew

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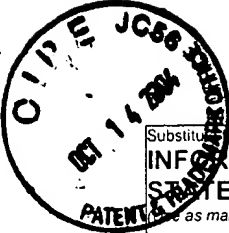
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

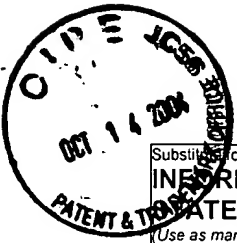
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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Attorney Docket No: 279.268US1

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		O'PHELAN, MICHAEL, et al., "METHOD OF CONSTRUCTING A CAPACITOR STACK FOR A FLAT CAPACITOR", <u>Application Ser. No. 10/882,144, Filed June 30, 2004</u> Attorney Docket No. 279.169US3, 57 pgs.	

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